Exporting IDA Debug Information

Overview

- Who am I?
- What's the problem?
- What does this tool do?
- How does it work?
- Demo

about:me



Library function Data	egular function Unexplored Instruction External symbol	
Functions window	O B IDA View-A X Pseudocode-A X O Hex View-1 X A Structures X	1
Function name		-
F sub_404130	64 11 (46 $= 13$)	
f sub_404140	66 goto LABEL 8;	
<u>f</u> sub_404160	67 }	
f sub_404180	68 if (!*(EYTE *)(v10 + 1))	
<u>f</u> j_free	70 if (46 != v13)	
F sub_404260	71 (
📝 sub_404500	72 LABEL 11:	
풁 sub_404550	73 1f (*(BYTE*)(VIO + 1) == 46 55 !*(BYTE*)(VIO + 2)) return 1:	
📝 sub_4045D0	75 goto LABEL 20;	
F sub_404820	76 }	
F sub_404890	77 LABEL 8:	
f sub_404A10	78 II (*(_BYTE *)(V9 + 1) == 46 && !*(_BYTE *)(V9 + 2)) 79 return (unsigned int)=1:	
f sub 404A20	80 if (46 == v14)	
F sub_404A30	 81 goto LABEL_11; 	
f sub 404A60	82 LABEL 20:	
F sub_404A70		
f sub 404A80	85 v11 = (const char *) (v9 + 1);	
f sub 404A90	• 86 ++v10;	
f sub 404AA0	37 11 (V14 == 46)	
7 sub 404C00	89 return (unsigned int)-1;	
F sub 404CE0	90 }	
7 sub 404DB0	91 if (v14 = 46)	
f sub 404E70	93LaBEL 22:	
7 sub 404EB0	• 94 v36 = v11;	
7 sub 404ED0	• 95 $v37 = v10;$	
7 sub 404F40	96 V16 = sub_40AD10 ((char **) & V36);	
7 sub 404FF0	98 $y_1 = (int 64)y_1 6;$	
- auk 405040	99 v19 = sub_40AD10 ((char **) &v37);	
1 Line 122 of 207	• 0100 v20 = v17 == 0LL;	
Line 152 01 557	$101 \sqrt{21} = \sqrt{17}$	
A Graph overview	• 103 v23 = (int64)v19;	
	104 if (v20)	
	105 V18 = (1ntb4)V36; 0.105 V18 = (1ntb4)V36; 0.105 V18 = (1ntb4)V36;	
	• 107 $v24 = v18 - (QWORD)v11;$	
1 A A A A A A A A A A A A A A A A A A A	0000AF35 sub 404A90:79	
.		



Function argument information has been propagated The initial autoanalysis has been finished

Why export information from IDA?

- An embedded device may have no way to connect IDA remotely
 - Manually referencing IDA is tedious
- Some platforms may have software debuggers that would be useful with debug info
- Some tools allow interesting dynamic analysis techniques not available with IDA
 - Ex: Reverse debugging

Use-case: QNX

- Provides a version of GDB for their platform on lots of architectures
 - Downside: it doesn't use the standard protocol
- Lots of connected components of mixed architecture
- Maybe no IP connections

With this plugin: export the debug info from IDA and import into gdb on the target.



Debug Info Formats

• STABS

- Designed in the 1980s
- Puts all info in symbol table
- Not well standardized

• DWARF

- \circ $\,$ Designed along with ELF $\,$
- Used by most modern compilers
- Binary format
- Windows CodeView/Program Database
 - Mostly undocumented, windows-only
- Many Others
 - COFF, OMF, IEEE-695



dwarfexport

dwarfexport is a plugin for IDA Pro that creates DWARF debug info using function names/variables locations/structures extracted from IDA.

It lets you create binaries as though you had built with debugging enabled.

Implementation

What do we need from IDA?

- Decompiled source
- 'step' points
- Global/local variable locations
- Type information



sub esp, 14h [ebp+var C], OAh mov sub esp, 8 push [ebp+var C] push offset format "%d" call printf add 10h esp.



– NumericLiteral 10

IDA AST

```
IDA View-A X Pseudocode-A X O Hex View-1 ,

1 int __cdecl main(int argc, const char **argv, cons

2 {

3 printf("%d", 10);

4 return 0;

5 }
```

```
printer.apply_to(cfunc.body, None)
0x804842e block
0x804842e call
0x804842e obj: printf
0x8048429 obj: %d
0x804841c num: 10
0x8048436 return
0x8048436 num: 0
```

```
Duthan
```

IDA A	ST
4	11 t C t 11 t 1010001 :
24	old *Iastcall sub_404820(164
3	char v3: // r1301
4	OWORD *v4; // rbx@1
5	int64 v5; // rang1
6	int64 v6; // rax@3
7	void *resul; // rax@5
8	
9	$v_3 = a_3;$
10	$v4 = sub_411DCO(0x20uLL);$
11	$V_{0} = 0LL;$
12	11 (d2)
11	$v_{3} = sub_{411}r_{A0}(a_{2});$
15	$v_{6} = 0 U_{1}$
16	if(al)
17	v6 = sub 411FA0(a1);
18	*v4 = v6;
19	result = ptr;
20	$*((_BYTE *)v4 + 16) = v3;$
21	ptr = v4;
22	v4[3] = result;
23	return result;
24 }	

```
printer.apply_to(cfunc.body, None)
0x404831 block
0x404831
           asg
0x404831
             var: v3
0x404831
             var: a3
0x40483d
           asg
0x40483d
             var: v4
0x404838
             cast: int64
0x404838
               call
0x404838
                  obj: sub_411DC0
                  num: 32
0x40482c
0x404840
           asg
0x404840
             var: v5
0x404840
             num: 0
0x404845
           if
0x40484a
             block
0x40484a
               asq
0x40484a
                 var: v5
0x40484a
                  call
                    obj: sub_411FA0
0x40484a
0x404847
                    var: a2
0x404845
             var: a2
0x40484f
           asg
0x40484f
             idx
0x40484f
               var: v4
0x40484f
               num: 1
0x40484f
             var: v5
0x404853
           asq
0x404853
             var: v6
0x404853
             num: 0
0x404858
           if
0x40485d
             block
0x40485d
               asq
0x40485d
                  var: v6
                  call
0x40485d
0x40485d
                    obi: sub 411FA0
```

	E auchar	
	printer.a	oply_to(cfunc.body, None)
	0x404831 1	DLOCK
	0x404831	asg
	0x404831	var: v3
	0x404831	var: a3
Stan Points	0x40483d	asg
	0x40483d	var: v4
	0x404838	cast:int64
	0x404838	call
	0x404838	obj: sub_411DC0
1void *fastcall sub_404820(int64	0x40482c	num: 32
2 {	0x404840	asg
3 char v3; // r1301	0x404840	var: vo
4 _QWORD *v4; // rbx@1	0x404840	num: 0
5int64 v5; // raye1	0x404845	
6int64 v6; // xax@3	0x40484a	DIOCK
7 void *result; // raxe5	0x40484a	asg
8	0x40484a	var: vo
9 $v3 = a3;$	0x40484a	Call
10 $v4 = sub_{411DC0} (0x20uLL);$	0.40484a	ODJ: SUD_411FA0
11 $v5 = 0LL;$	0.404847	Var: az
12 if (a2)	0x404845	Var: az
13 $v5 = sub 411FA0(a2);$	0x404841	asy
) 14 $v4[1] = v5;$	0x404041	
15 $v6 = 0LL;$	0x404041	var. va
16 if (a1)	0x40484f	war: w5
17 $v6 = sub 411FA0(a1);$	0x404853	
18 $*v4 = v6;$	0x404853	var: v6
19 result = ptr;	0x404853	num: 0
20 *((BYTE *) $v4 + 16$) = $v3$;	0x404858	if
21 $ptr = v4;$	0x40485d	block
22 v4[3] = result;	0x40485d	asg
23 return result;	0x40485d	var: v6
24}	0x40485d	call
	0x40485d	obi: sub 411FA0

Local Variables

- Stack Variables:
 - Location is expressed as an offset from frame base address
 - Note: There is no (complete) SDK interface for this
- Register Variables:
 - Translate the IDA register number to dwarf number

Type Information

As the binary is traversed, maintain a mapping of `tinfo_t` to DWARF `die`:

- Extract each struct member name and type, as well as the offset from the struct start
- Handle array/pointer types

_			
<	< 1><0x0000021d>	DW_TAG_structure_type	
		DW AT name	lua State 0
		DW AT byte size	0×0 <u>0</u> 0000d <u>0</u>
		DW AT sibling	<0x00003b3>
<	< 2><0x0000022f>	DW TAG member	
		DW AT type	<0x00003b3>
		DW_AT_name	next
		DW AT data member l	ocation DW OP plus uconst 0
<	< 2><0x0000023c>	DW_TAG_member	
		DW_AT_type	<0x000003f2>
		DW_AT_name	tt
		DW_AT_data_member_l	ocation DW_OP_plus_uconst 8
<	< 2><0x00000247>	DW_TAG_member	
		DW_AT_type	<0x000003f2>
		DW_AT_name	marked
		DW_AT_data_member_l	ocation DW_OP_plus_uconst 9
<	< 2><0x00000256>	DW_TAG_member	
		DW_AT_type	<0x000003fc>
		DW_AT_name	nci
		DW_AT_data_member_l	ocation DW_OP_plus_uconst 10
<	< 2><0x00000262>	DW_TAG_member	

Demo

Other Uses

- Add debug info for shared libraries and create a fully debugged environment
- Reverse-debugging
 - Tested using 'rr' on linux
- Hardware Debugging
 - Software frontends for hardware debuggers must use some debug format
 - Green Hill 'MULTI' IDE can import DWARF info

☆ Debug ¤	🐜 i� 🍸 🗖 🖾 (×)= Varia	ables 🛛 🎭 Breakpoints 🕮 R	egisters 🛋 Modules 👘 🖄 🛤 🖻	C 2
Clua [C/C++ Application]	Name		Туре	Va
👻 🎲 lua [17241] [cores: 2]	(X)=V3	R	int	
👻 🔊 Thread #1 [lua] 17241 [core: 2] (Suspende	d : Breakpoint) 🙌 st	atus	int	
≡ main() at a.out.c:20,470 0x42907e	· - + L		struct lua_State_0 *	
■libc_start_main() at 0x7ffff774343a	> •	nevt	struct GCObject 0 *	
start() at a.out.c:1 0x401a9a	(×):	itt	lu byte	
▶∃ gap (7.12.1)	(x):	marked	lu_byte	
	(v):	'nci	unsigned intint16	
	(x):	status	lu_byte	
		top	struct TValue *	
		^(x) ⁼value_	Value_0	
		⇔=tt_	int	
	> •	l_G	struct global_State_0 *	
	*	ci	struct CallInfo_0 *	
	>.+	oldpc	const Instruction *	
lua.cpp				
20461				
20462	Construction and Construction			
20463 intcdecl main(int argc	<pre>, const char **argv, const char **envp)</pre>			
20404 1				
20465 int v3: // eax02				
20465 int v3; // eax@2 20466 int result; // ST10 4@3				
20465 int v3; // eax@2 20466 int result; // ST10_4@3 20467 int status; // [rsp+14h] [rbp-Ch]@3			
20465 int v3; // eax@2 20466 int result; // ST10_4@3 20467 int status; // [rsp+14h 20468 lua_State_0 *L; // [rsp-] [rbp-Ch]@3 +18h] [rbp-8h]@1			
20465 int v3; // eax@2 20466 int result; // ST10_4@3 20467 int status; // [rsp+14h 20468 lua_State_0 *L; // [rsp 20469] [rbp-Ch]@3 +18h] [rbp-8h]@1			
<pre>20465 int v3; // eax@2 20466 int result; // ST10_4@3 20467 int status; // [rsp+14h 20468 lua_State_0 *L; // [rsp 20469 * 20470 L = luaL_newstate(); 20471 if (L)</pre>] [rbp-Ch]@3 +18h] [rbp-8h]@1			
<pre>20465 int v3; // eax@2 20466 int result; // ST10_4@3 20467 int status; // [rsp+14h 20468 lua_State_0 *L; // [rsp 20469 * 20470 L = luaL_newstate(); 20471 if (L) 20472 {</pre>] [rbp-Ch]@3 +18h] [rbp-8h]@1			
<pre>20465 int v3; // eax@2 20466 int result; // ST10_4@3 20467 int status; // [rsp+14h 20468 lua_State_0 *L; // [rsp 20469 * 20470 L = luaL_newstate(); 20471 if (L) 20472 { 20473 lua_pushcclosure(L, ())</pre>] [rbp-Ch]@3 +18h] [rbp-8h]@1 lua_CFunction)pmain, θ);			
20465 int v3; // eax@2 20466 int result; // ST10_4@3 20467 int status; // [rsp+14h 20468 lua_State_0 *L; // [rsp 20469 v20470 L = luaL_newstate(); 20471 if (L) 20472 { 20473 lua_pushcclosure(L, (20474 lua_pushinteger(L, arg] [rbp-Ch]@3 +18h] [rbp-8h]@1 lua_CFunction)pmain, 0); gc);			
<pre>20465 int v3; // eax@2 20466 int result; // ST10_4@3 20467 int status; // [rsp+14h 20468 lua_State_0 *L; // [rsp 20469 • 20470 L = luaL_newstate(); 20471 if (L) 20472 { 20473 lua_pushcclosure(L, (20474 lua_pushinteger(L, arg 20475 lua_pushlightuserdata 20476 ctteus_e_lua_posll/()</pre>	<pre>] [rbp-Ch]@3 +18h] [rbp-8h]@1 lua_CFunction)pmain, 0); gc); (L, argv); 2</pre>			

Eclipse



File Ed	it Selection View Go Debug Help		
ß	DEBUG 🕨 (gdb) Lat 🔻 🏟 🗵	C main.c C a.our 🗄 🕨 🗘 🏌 🏷 💻	ශි 🎞
ך א א	✓ VARIABLES ✓ Locals v3: 6557720 status: 32767 ✓ L: 0x641018 ▶ next: 0x0	20455 { 20456 result = 0LL; 20457 } 20458 } 20459 return result; 20460 20461 20462	
Ē	marked: 1 '\001' nci: 0 status: 0 '\000' ✓ top: 0x641640 value_: 0 tt_: 0 ▶ 1_G: 0x6410e8	<pre>20463 intcdecl main(int argc, const char **argv, const char **envp) 20464 { 20465 int v3; // eax@2 20466 int result; // ST10_4@3 20467 int status; // [rsp+14h] [rbp-Ch]@3 20468 lua_State_0 *L; // [rsp+18h] [rbp-8h]@1 20469 20470 L = luaL_newstate(); 20471 if (_L)</pre>	
	▲ CALL STACK PAUSED ON STEP main() a.out.c 20471:1 libc.so.6!_libc_start_m	<pre>20472 { 20472 { 20473 lua_pushcclosure(L, (lua_CFunction)pmain, 0); 20474 lua_pushinteger(L, argc); 20475 lua_pushlightuserdata(L, argv); 20476 status = lua_pcallk(L, 2, 1, 0, 0LL, 0LL); 20477 result = lua_toboolean(L, -1); 20478 report(L, status); 20479 lua_close(L); 20490 v3 = lresult 11 status;</pre>	
	→ BREAKPOINTS a.out.c 1:1 BREAKPOINTS a.out.c 20470 main.c 4 main.c 5	<pre>PROBLEMS OUTPUT DEBUG CONSOLE TERMINAL =thread-group-added,id="i1" GNU gdb (GDB) 7.12 Copyright (C) 2016 Free Software Foundation, Inc. License GPLv3+: GNU GPL version 3 or later <http: gnu.org="" gpl.html="" licenses=""> This is free software: you are free to change and redistribute it. There is NO WARRANTY, to the extent permitted by law. Type "show copying"</http:></pre>	簷 ^
004		main/int areas const char ** area const char ** anua) In 20071 Col 1 Spaces 2 LITE-9 IE	

VS Code

onfiguration:	Active(Debug)	Platform: Active(x64)	~	Configuration Manager.
Configurat Genera	ion Properties I	Look for options or switches:		
Debug	ging			
VC++ 1	Directories	8 9. 8		
▲ C/C++		Additional Include Directories		
Ger	timination	Additional Ontions	-fno_delayed_template_parsing %(Ad	ditional(Ontions)
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Co	de Generation	C Language Standard	Default	
Lan	quage	Compile As	Default	
Pre	compiled Headers	Compile Precompiled Header As	Compile as C++ Code (-v c++-beade	r
Adv	vanced	Debug Information Format	Full Debug Information (DWARE2) (-o	2 - adwarf-2)
All	Options	Enable Advanced SIMD(Neon)	Tan bebag mornadon (brit da e) (g	je ganon ej
Cor	mmand Line	Enable C++ Exceptions	Ves (-fexceptions)	
▷ Linker		Enable Data-Level Linking	No	
▷ Manife	st Tool	Enable Function-Level Linking	No	
▷ XML D	ocument Generator	Enable Run-Time Type Information	Yes (-frtti)	
Browse	Information	Enable Verbose mode	No	
▷ Build E	vents	Floating-point ABI		
D Custon	n Build Step	Forced Include Files		
Code Analysis	Microsoft Compatibility Mode	No (-fno-ms-compatibility)		
	Microsoft Compatibility Mode Version			
	Additional Include Directories		and the second	
		Specifies one or more directories to add to	the include path; separate with semi-colon:	s if more than one. (-I[path

Visual Studio(?)

Limitations

- DWARF debug info is not useful for windows utilities
- Limitations in IDA SDK may make some debug info inaccurate (for now)
- Register number translations must be added on a per-architecture basis
- Local variable values don't display correctly under GDB 8 (released June 4)



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